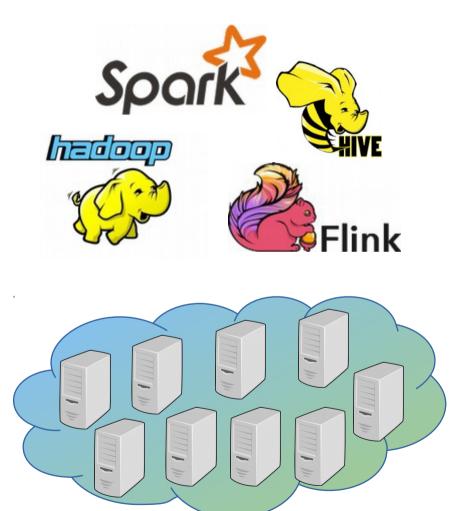


FlashNet: Flash/Network Stack Co-Design

<u>Animesh Trivedi</u>, Nikolas Ioannou, Bernard Metzler, Patrick Stuedi, Jonas Pfefferle, Ioannis Koltsidas, Kornilios Kourtis, and Thomas R. Gross

IBM Research and ETH Zurich, Switzerland





- data intensive
- run on 100-1000s of servers
- performance depends upon <u>both</u> network and storage



 performance depends upon <u>both</u> network and storage



- StackMap: Low-Latency Networking with the OS Stack and Dedicated NICs, *USENIX'16*
- Network Stack Specialization for Performance, SIGCOMM'14
- mTCP: A Highly Scalable User-level TCP Stack for Multicore Systems, *NSDI'14*
- MegaPipe: A New Programming Interface for Scalable Network I/O, *OSDI'12*
- ...

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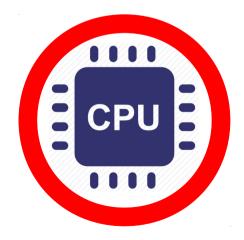
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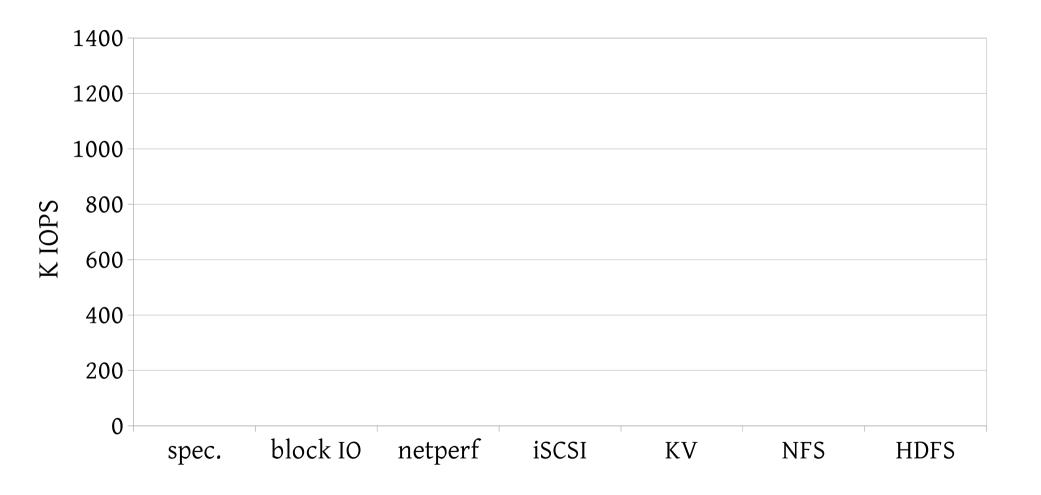
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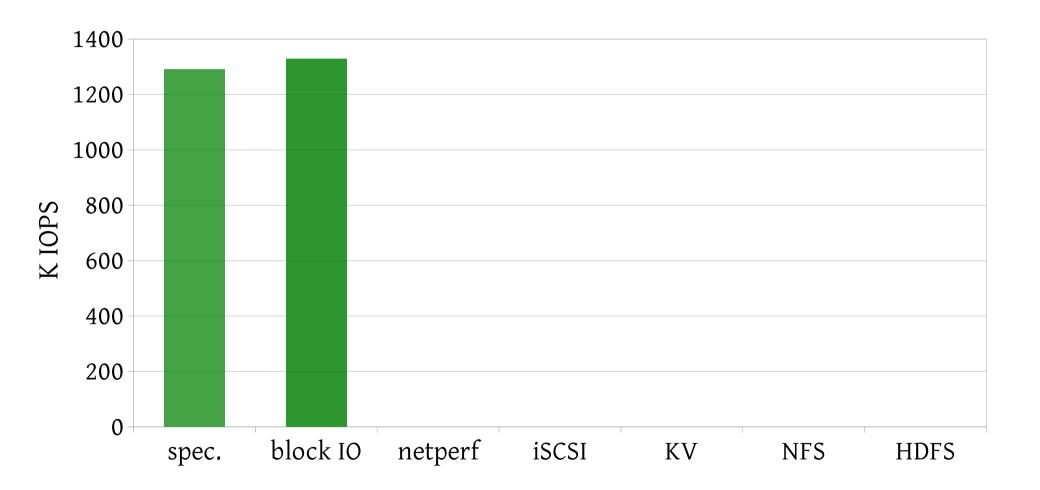


The Cost of the Gap



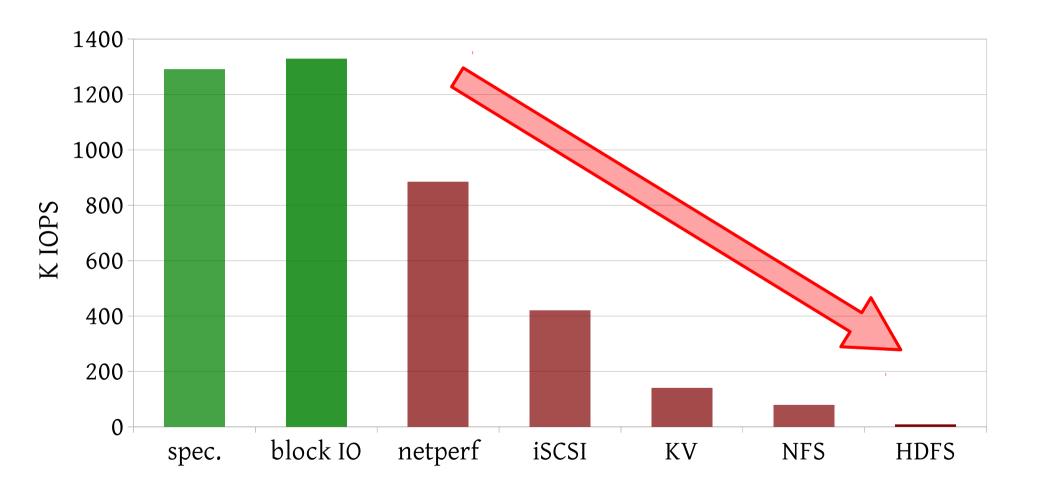


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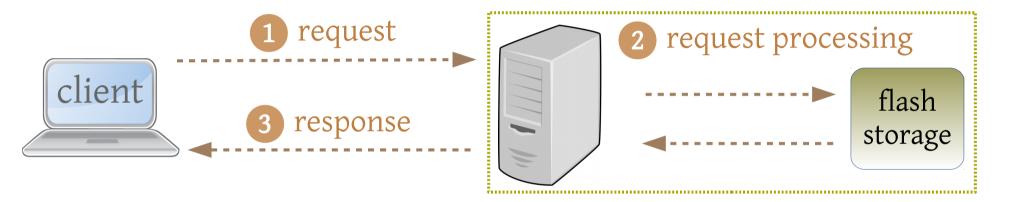




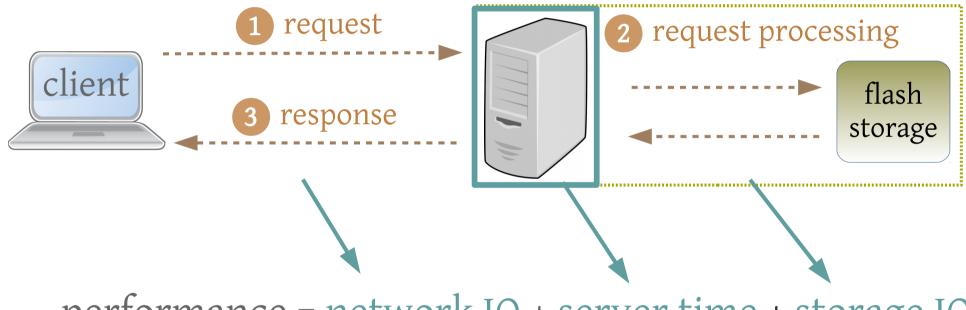






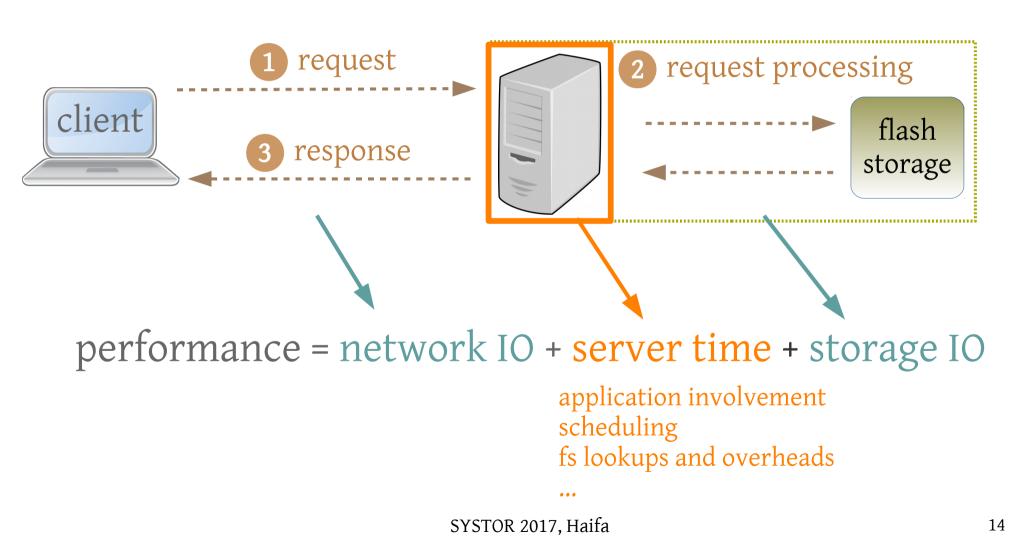






performance = network IO + server time + storage IO







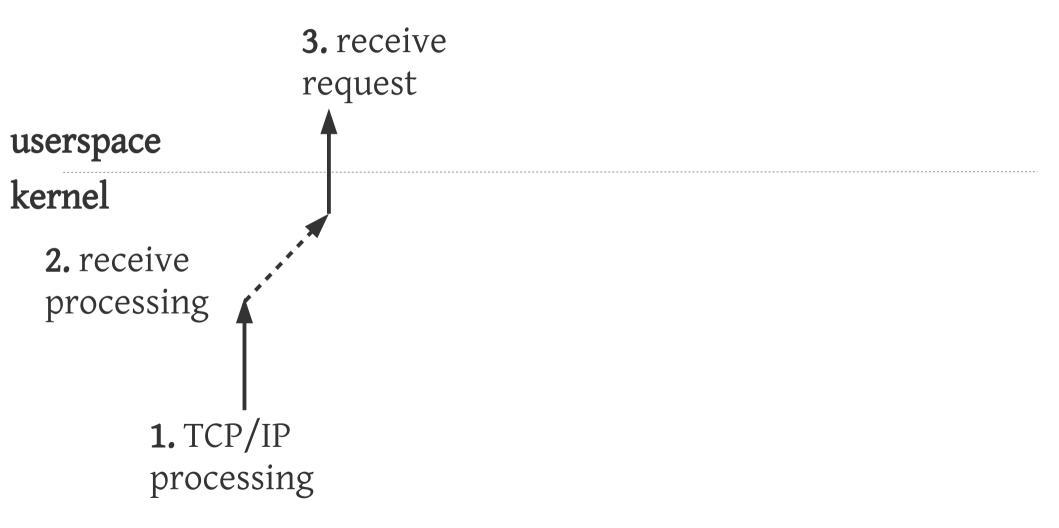
userspace

kernel

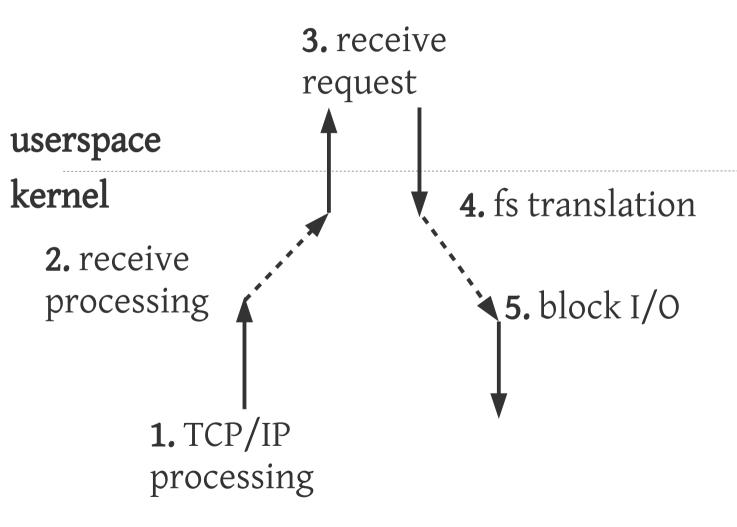
1. TCP/IP processing

SYSTOR 2017, Haifa

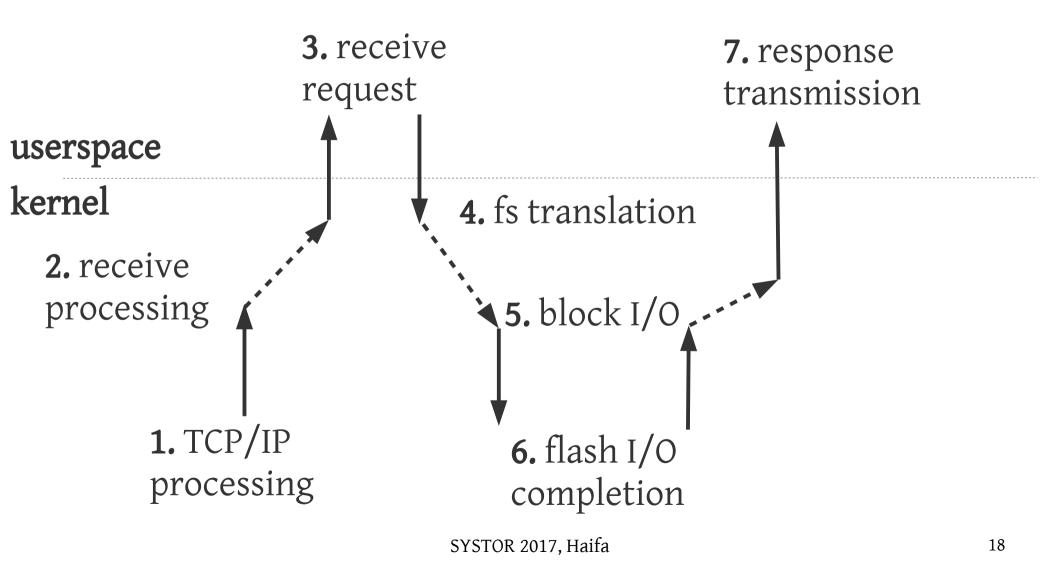




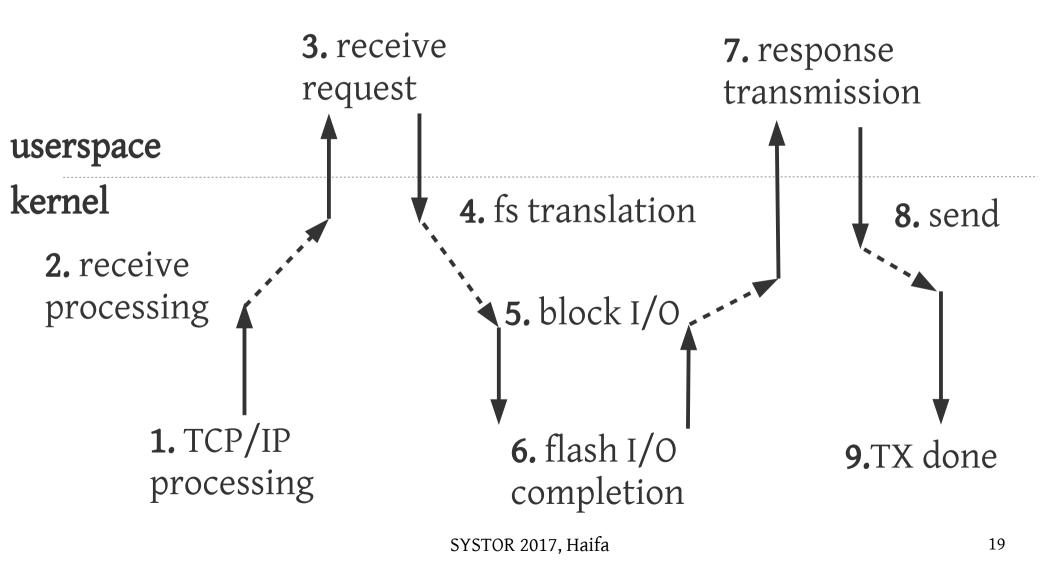




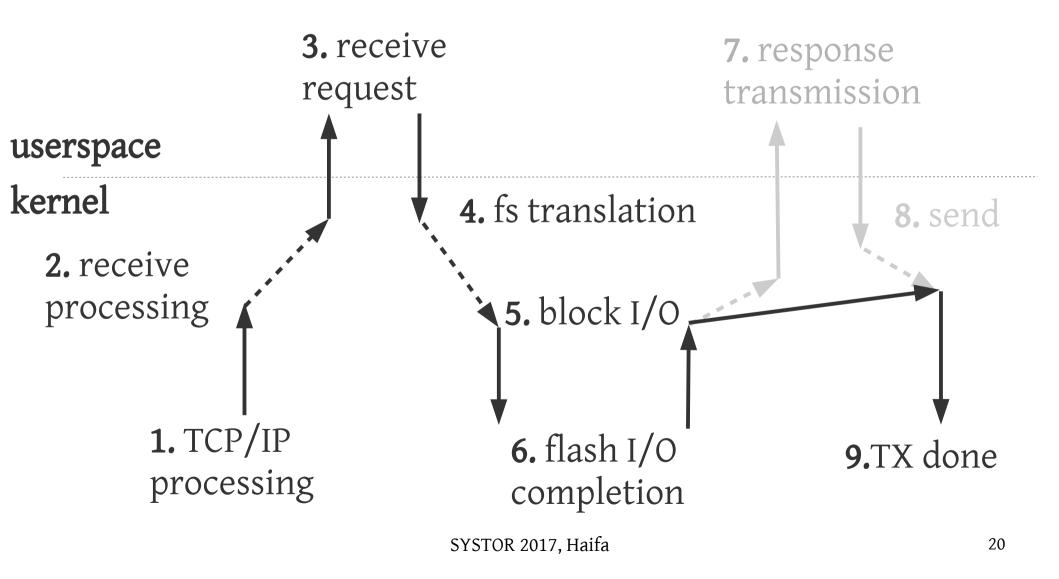




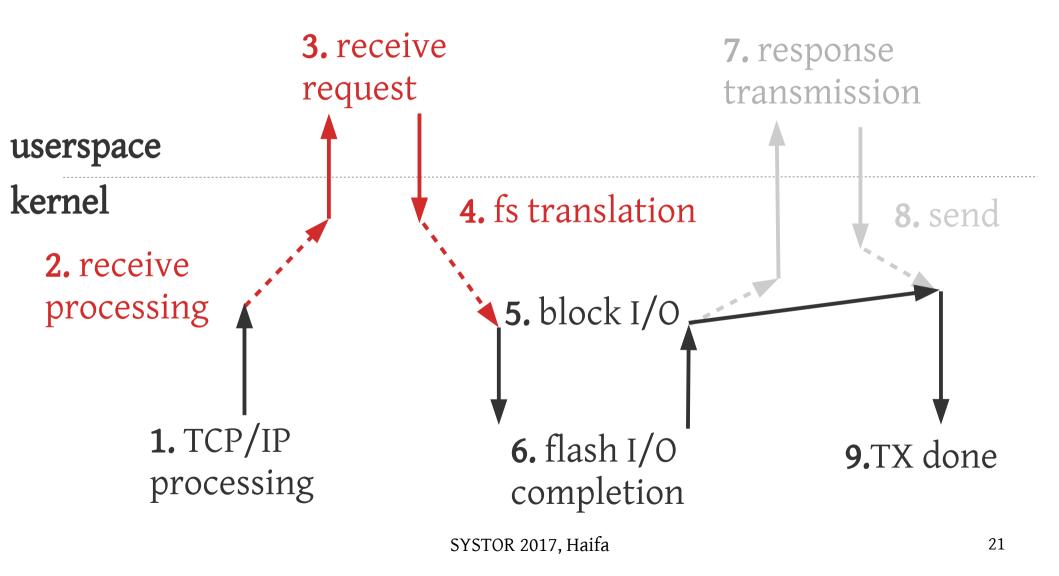




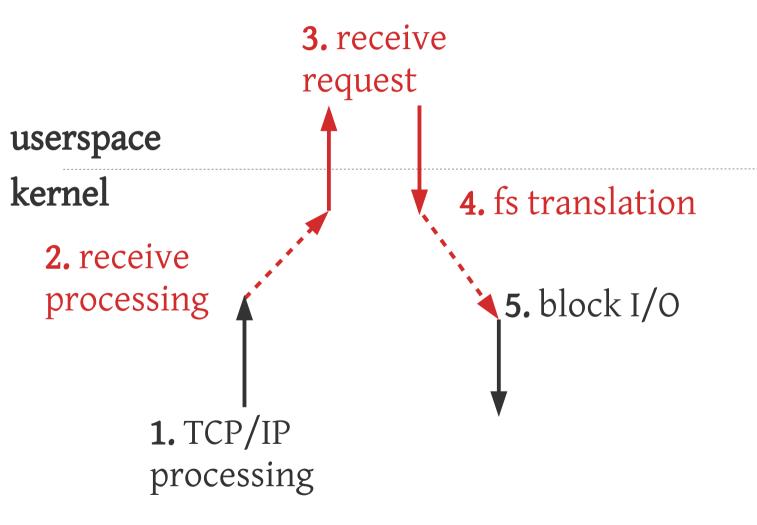




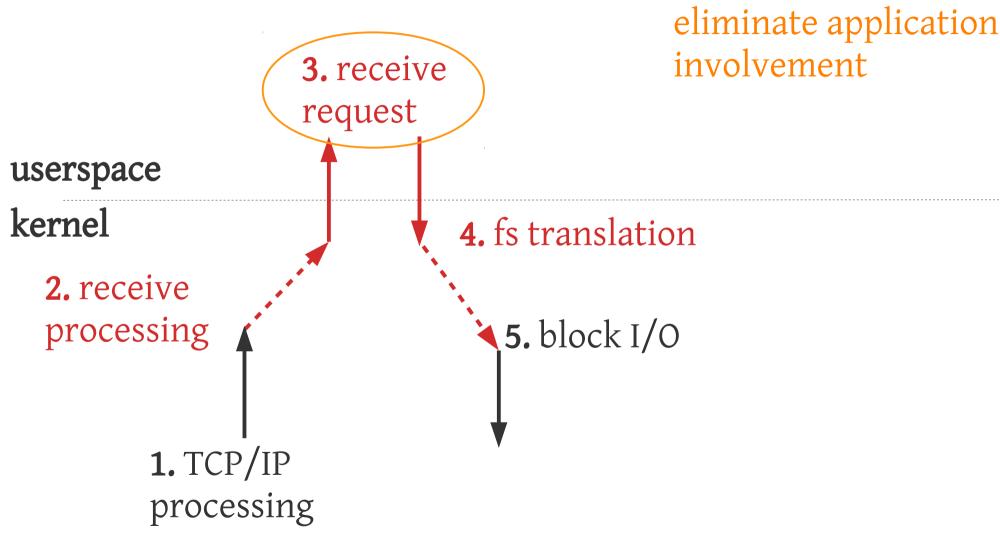




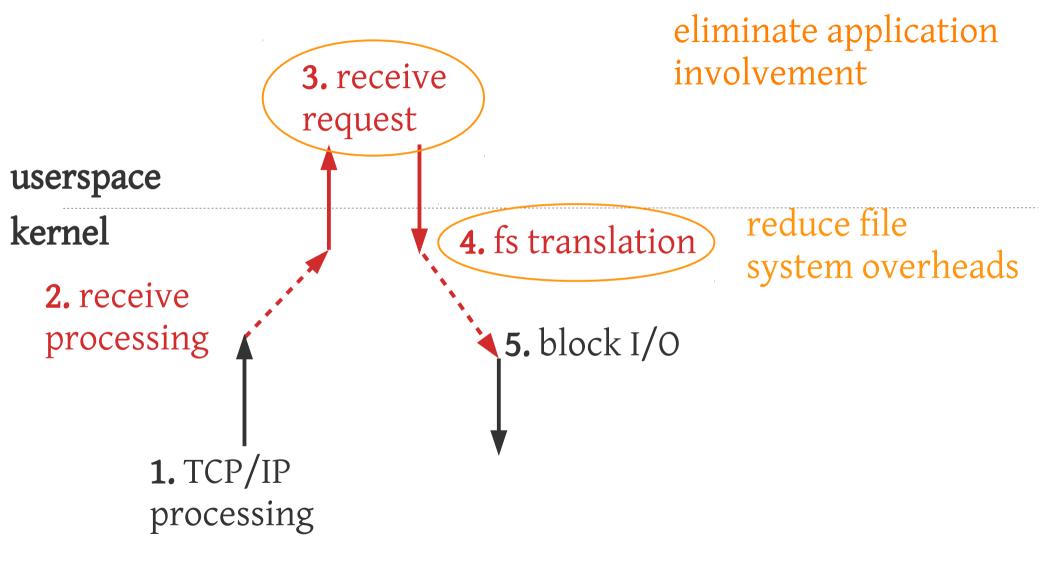




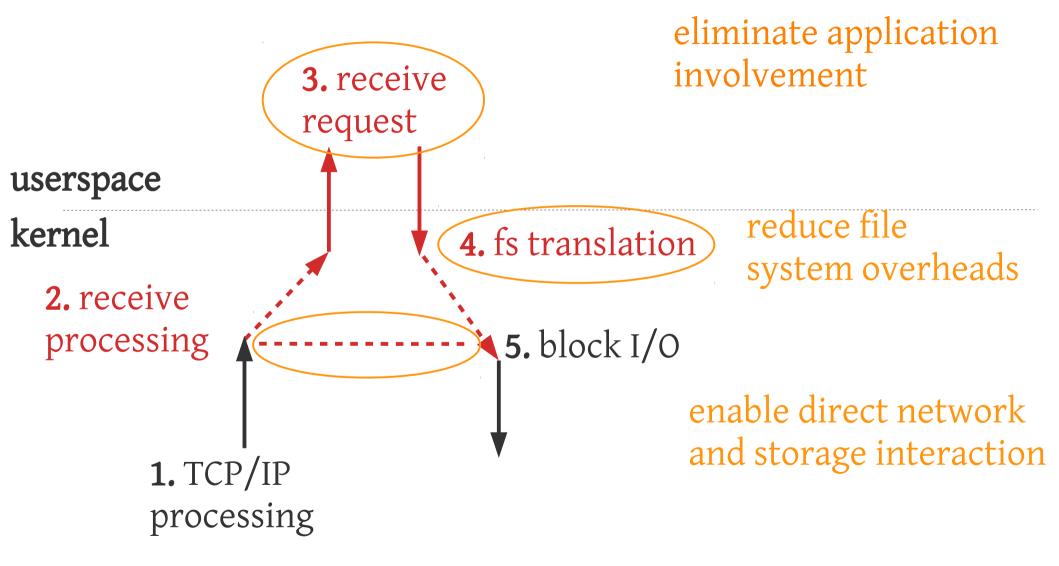




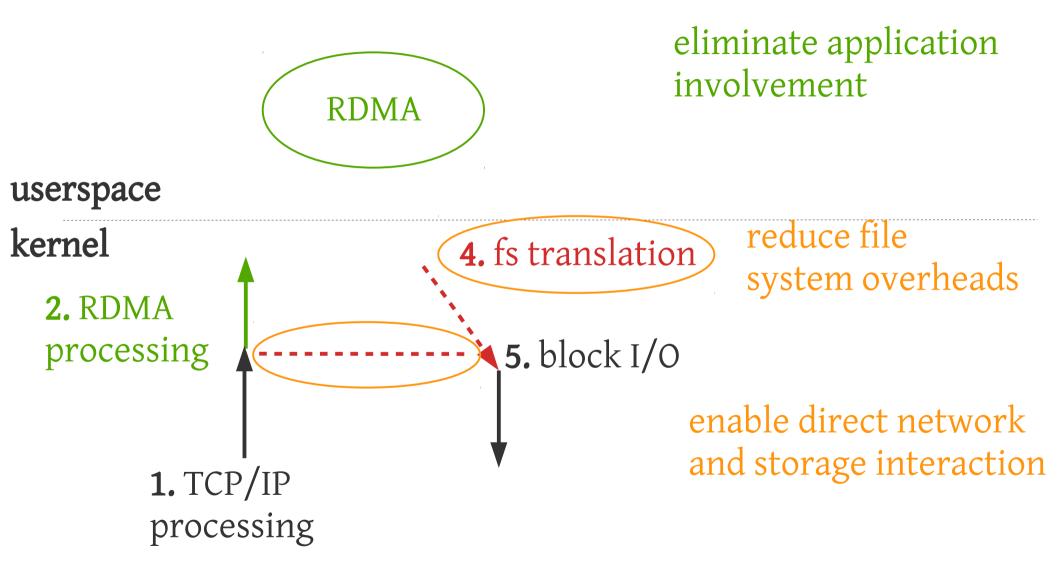




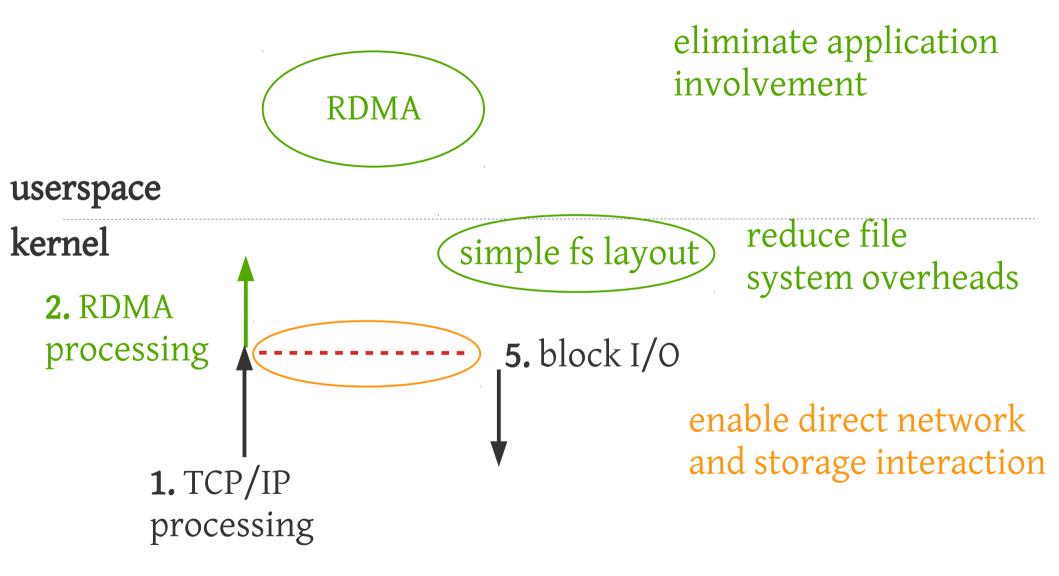




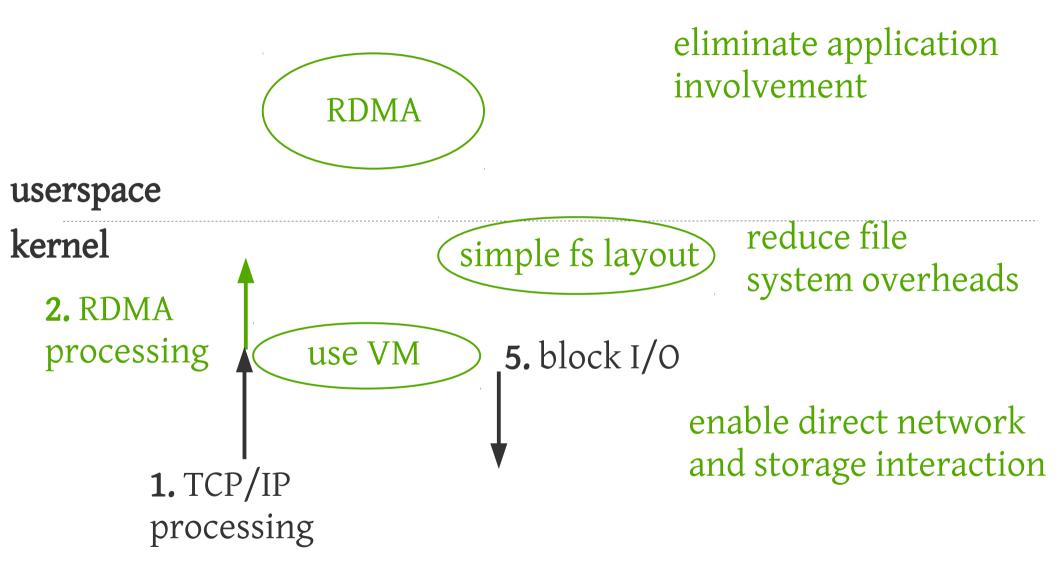




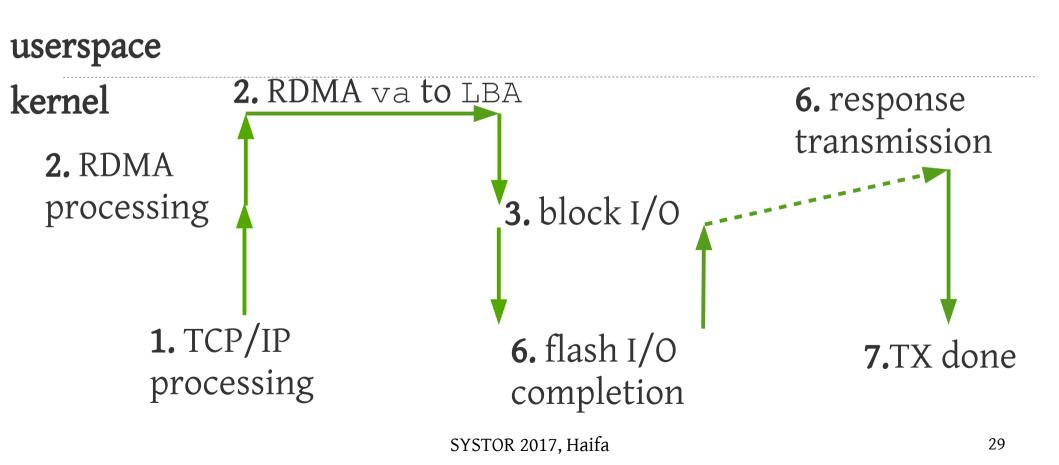




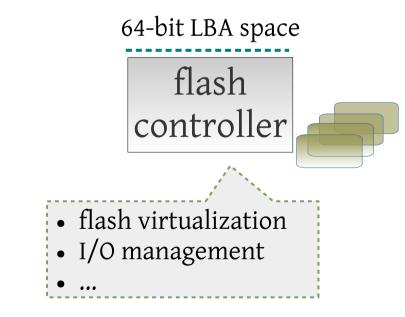




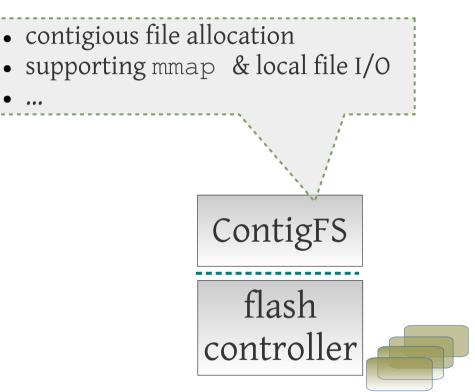




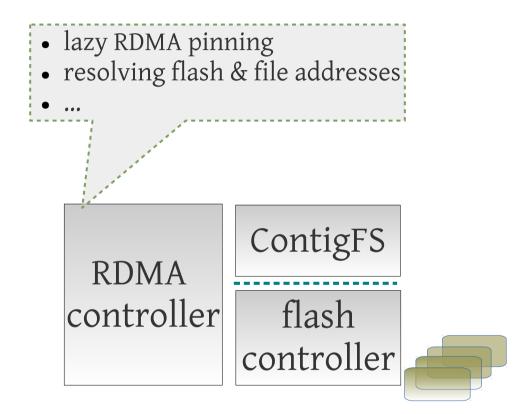




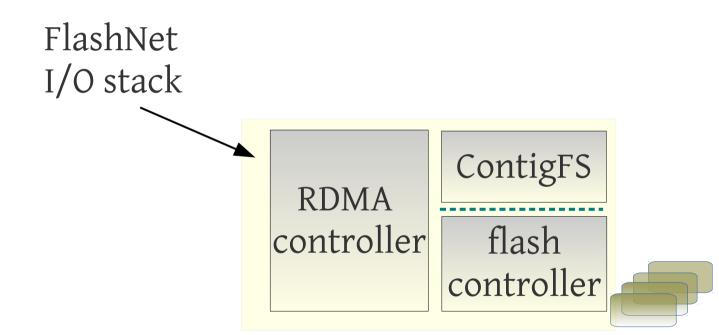




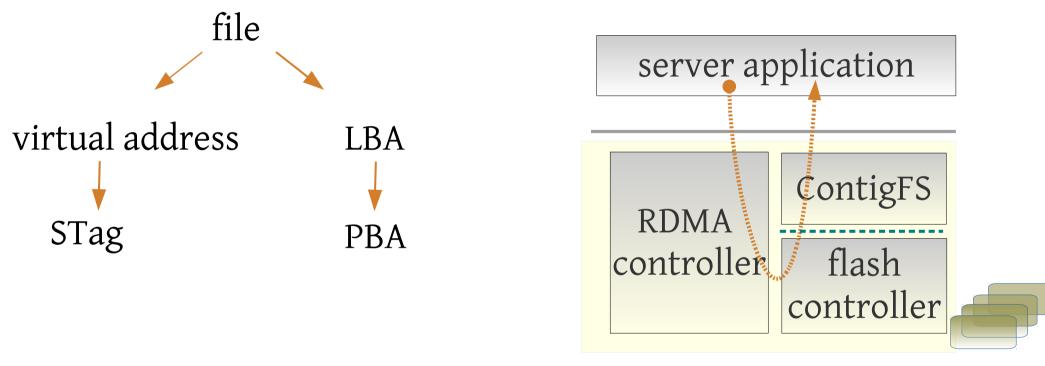






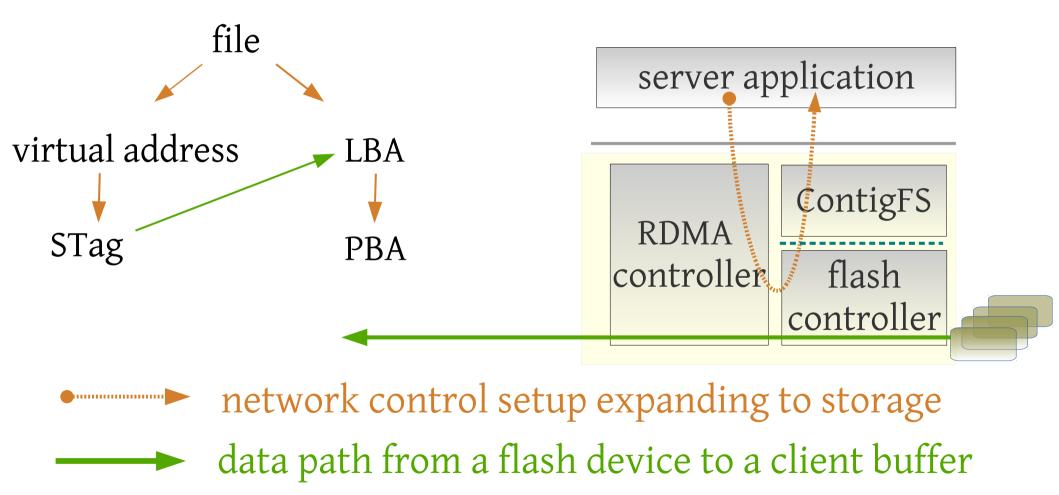




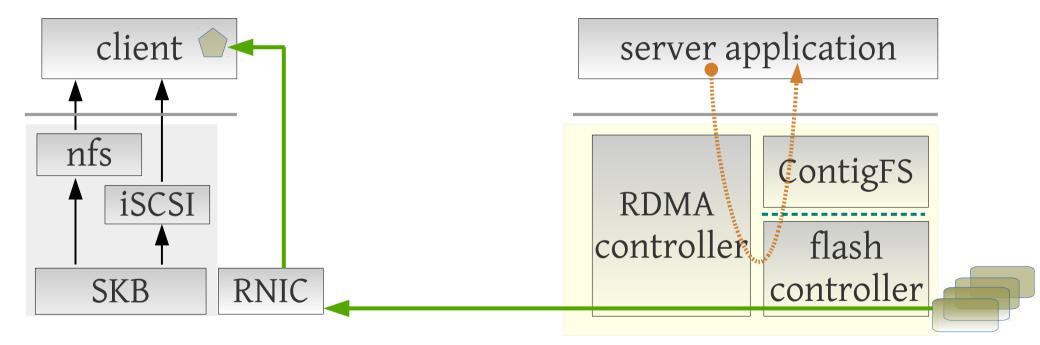


network control setup expanding to storage









network control setup expanding to storage
 data path from a flash device to a client buffer



Performance Evaluation

How efficient is FlashNet's IO path? Does it help with applications?

...more in the paper

9-machine cluster testbed

- CPU : dual socket E5-2690, 2.9 GHz, 16 cores
- DRAM : 256 GB, DDR3 1600 MHz
- NIC : 40Gbit/s Ethernet

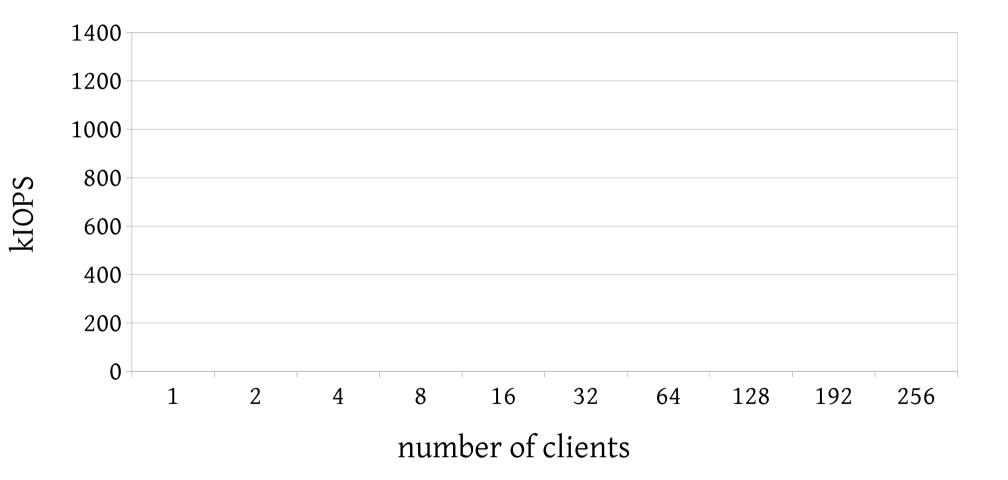
3xNVMe

Flash : 6.6 GB/sec (read), 2.7 GB/sec (write) peak 4kB read IOPS: 1.3 M

SYSTOR 2017, Haifa

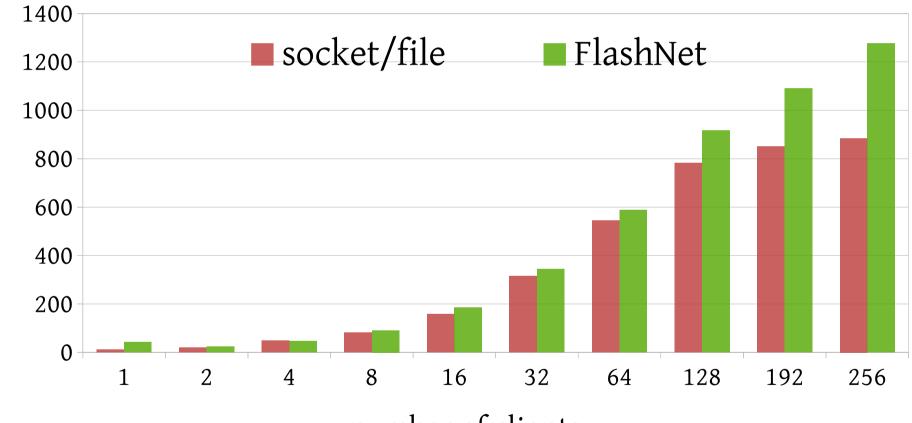


Performance - IOPS Efficiency





Performance - IOPS Efficiency

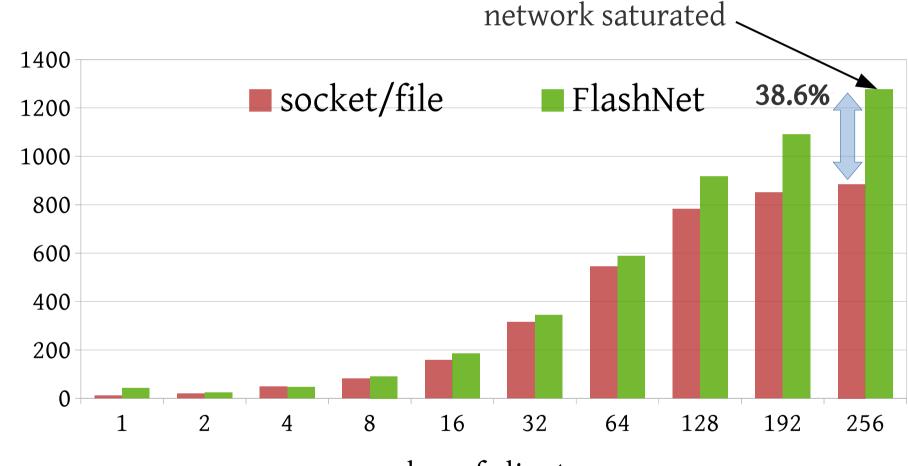


kIOPS

number of clients



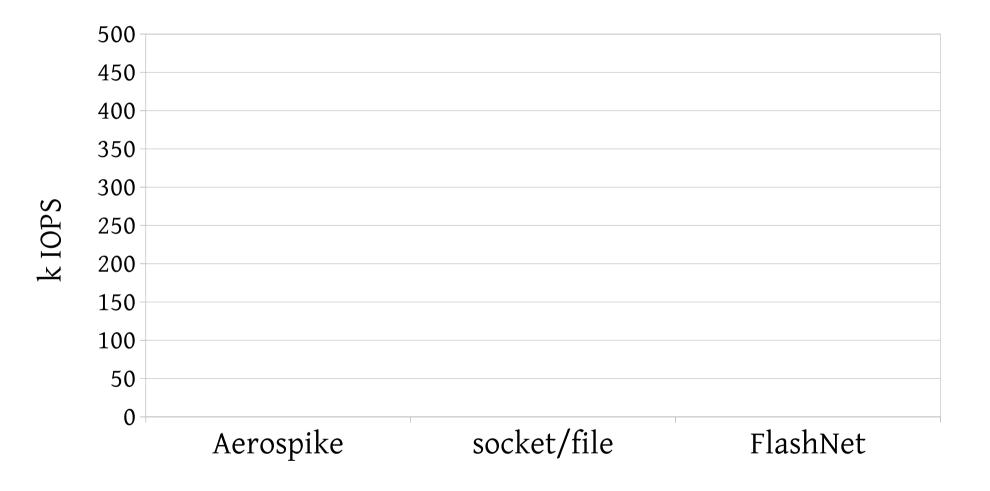
Performance - IOPS Efficiency



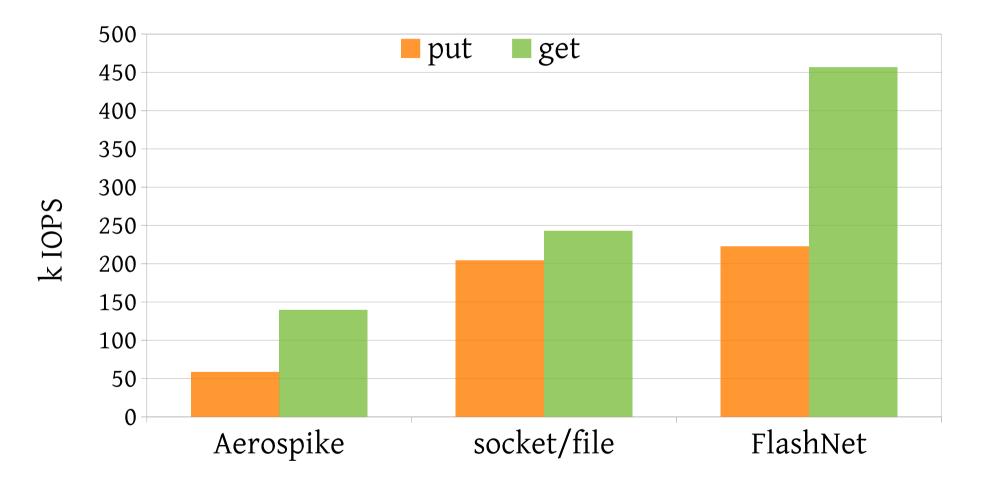
kIOPS

number of clients

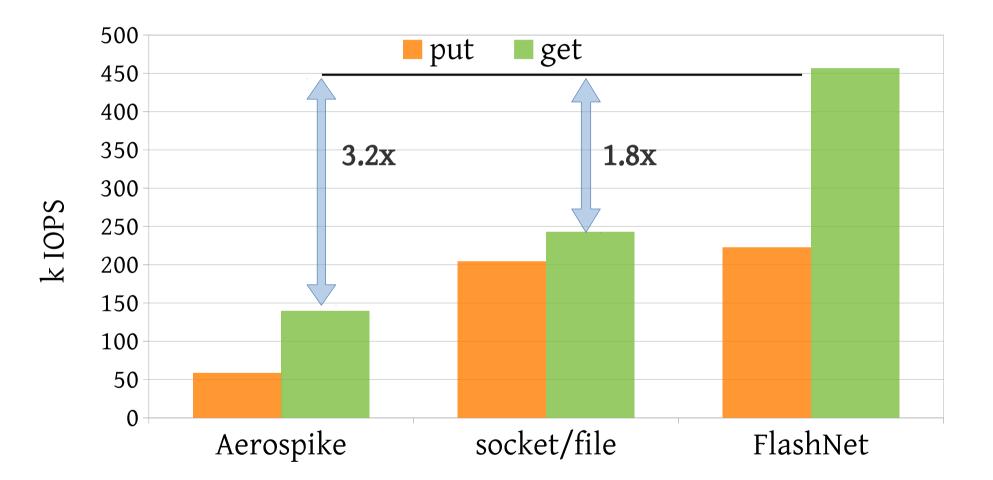
Application-level Performance: KV



Application-level Performance: KV



Application-level Performance: KV





Conclusion

Identified performance issues with networked flash

Apply RDMA principles and concepts by extending the path separation idea to a flash controller and a file system

FlashNet is a concrete implementation of this idea
demonstrated its capabilities in micro-benchmarks and applications

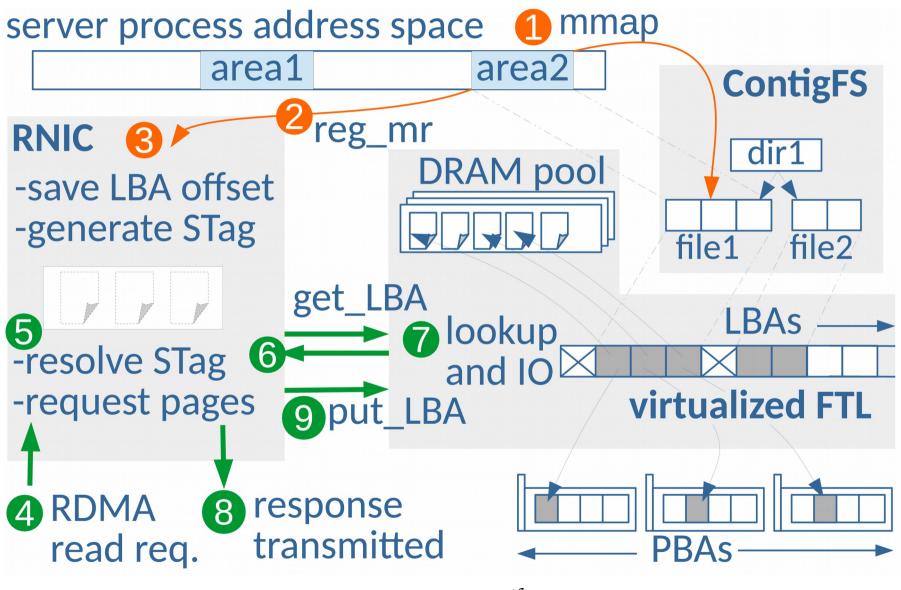
Excited to explore new use-cases for FlashNet



Thank you



Unified I/O Life Cycle





CPU Cycles Breakdown

| | network | storag | e device drivers | scheduling | kernel | request processing | misc |
|-------------|---------|--------|---------------------|------------|--------|--------------------|------|
| Socket/file | 19.3% | 7.3% | 6.7% | 15.8% | 40.1% | 4.7% | 6.1% |
| FlashNet | 20.6% | 0.8% | 6.4% | 8.4% | 46.7% | 11.7% | 5.4% |